



## NITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Keiichiro KATA & Shinichi CHIKAKI

Title: PROCESS FOR MANUFACTURING SEMICONDUCTOR DEVICE AND

SEMICONDUCTOR WAFER

Serial No.: 10/645,782

Filed: August 22, 2003

Examiner: Mark Prenty

Art Unit: 2822

**AMENDMENT AND REPLY UNDER 37 C.F.R. 1.173** 

Box AF

Commissioner of Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

This amendment and reply is being filed in response to an Office Action dated November 17, 2005.

Amendments to the Claims begin on page 2 of this document.

Remarks/Arguments begin on page 4 of this document.

Please amend the application as follows: